

Fig.4.

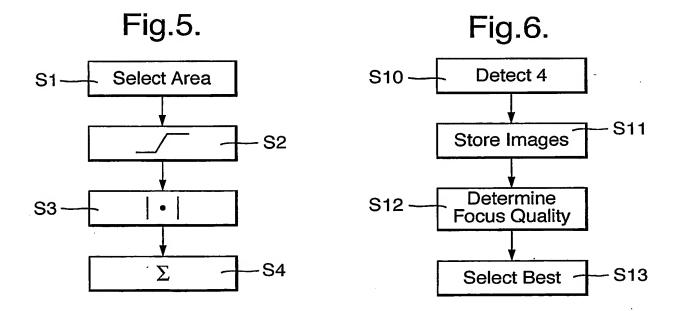
12

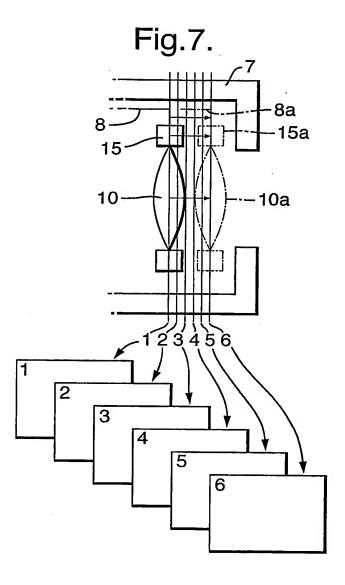
13

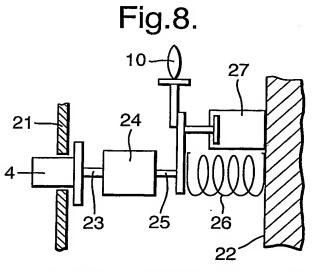
Proc. Memory

4

Controller



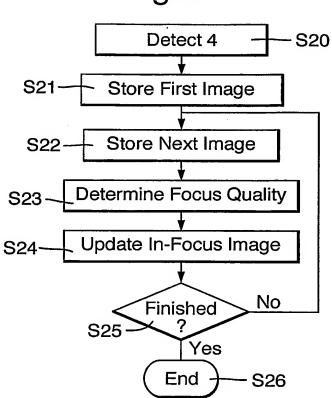


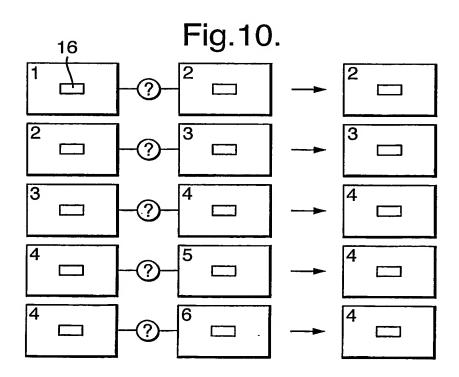


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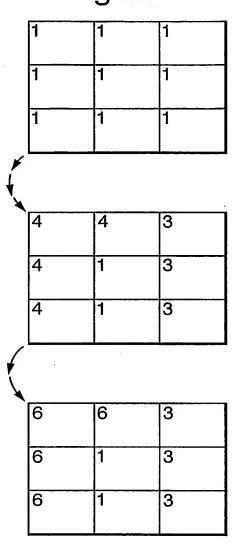
Fig.9.

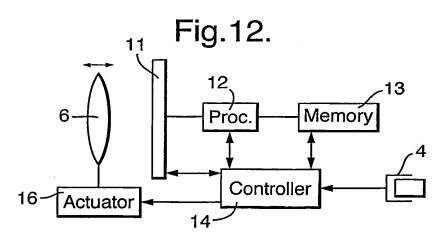




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Fig.11.





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